

# 20/ Req. for recon  
V. Bui  
3/21/03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of	)
	) Atty. Docket No.: <b>TM&amp;K0004</b>
Shinji TAKEDA et al.	)
	)
Serial No. 09/543,247	) Group Art Unit: 2814
	)
Filed: April 5, 2000	)
	) Examiner: David E. Graybill
For: SEMICONDUCTOR DEVICE AND	)
PROCESS FOR FABRICATION	)
THEREOF	) Date: December 2, 2002

**RESPONSE (C) AFTER FINAL**

BOX: AFTER FINAL  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the official Final Office Action dated June 6, 2002 regarding the above captioned application, Applicants make the following remarks.

**REMARKS**

As an initial matter, Applicants have submitted herewith a certified English translation of Japanese priority document 7-171154, filed July 6, 1995 in Japan. The USPTO has already received a certified copy of the original document (see Office Action Summary, Office Action dated September 10, 2001), and Applicants have properly claimed priority to Japanese document 7-171154, thereby perfecting a priority date of July 6, 1995 for the present application in accordance with 37 C.F.R. 1.55.